

10682410\_CLS1.txt  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10682410 on June 14, 2005

Original Classifications

6 710/22  
4 710/56  
3 718/104  
2 710/25  
2 710/26  
2 710/28  
2 710/29  
2 710/5  
2 710/71

710/24 OR  
308

710/22 - 28

~~28~~ - 35

52 - 57

208 - 212

111/152 - 154

111 - 113

Cross-Reference Classifications

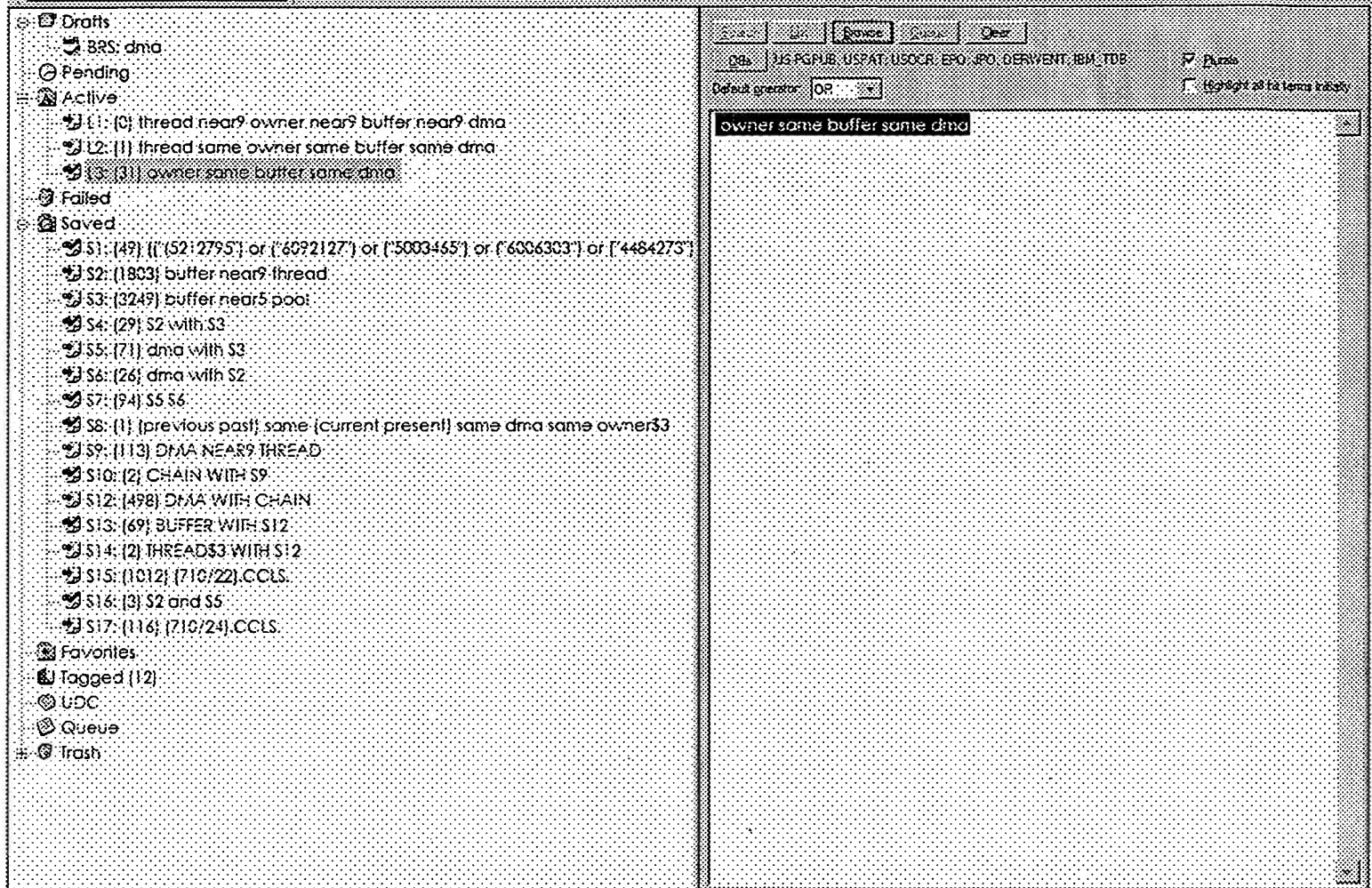
9 710/22  
4 710/36  
3 710/113  
3 710/240  
3 710/28  
3 711/152  
3 711/154  
2 370/229  
2 710/10  
2 710/100  
2 710/104  
2 710/25  
2 710/33  
2 710/35  
2 710/37  
2 710/40  
2 710/52  
2 711/100  
2 711/146  
2 711/151  
2 711/163  
2 711/202  
2 711/205  
2 714/3  
2 714/30  
2 714/4  
2 714/43  
2 714/56

Combined Classifications

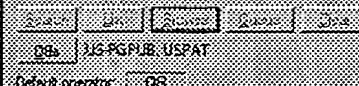
15 710/22  
5 710/28  
4 710/113  
4 710/25  
4 710/36  
4 710/56  
4 718/104  
3 710/240  
3 710/26  
3 711/100  
3 711/151  
3 711/152  
3 711/154  
2 370/229  
2 709/229  
2 710/10  
2 710/100

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2 710/104  
2 710/108  
2 710/200  
2 710/244  
2 710/29  
2 710/33  
2 710/35  
2 710/37  
2 710/40  
2 710/5  
2 710/52  
2 710/71  
2 710/8  
2 711/142  
2 711/146  
2 711/163  
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2 714/43  
2 714/56















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- [illegible]

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	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Inv
1		US 5619720 A	19970408	26	Digital signal processor having link ports for point-to-point communication	710/38			Garde; Doug
2		US 6092127 A	20000718	13	Dynamic allocation and reallocation of buffers in links of chained DMA over	710/56	710/22;		Tauscheck; Er
3		US 5828903 A	19981027	19	System for performing DMA transfer with a pipeline control switching such	710/53	710/26;		Sethuram; Ja
4		US 6324599 B1	20011127	11	Computer system and method for tracking DMA transferred data within	710/26	710/22;		Zhou; Ning e
5		US 6324598 B1	20011127	12	Software enlarged tag register and method thereof for notifying the complet	710/24	710/22;		Olson; Steve
6		US 5850570 A	19981215	13	Communication apparatus for performing data transfer utilizing direct	710/24	710/62;		Shoji; Fumio
7		US 5933654 A	19990803	14	Dynamic buffer fracturing by a DMA controller	710/23	710/18;		Galdun; Dar
8		US 20050033874 A1	20050210	11	Direct memory access using memory descriptor list	710/22	710/22		Futrai; William
9		US 6718405 B2	20040406	9	Hardware chain pull	710/22	710/24;		Rogers; Jeffr
10		US 5781799 A	19980714	16	DMA controller arrangement having plurality of DMA controllers and buffer	710/22	710/308;		Leger; Gear
11		US 20040267992 A1	20041230	17	Read/write command buffer pool resource management using read-pat	710/52			Jackson; Tho
12		US 6336150 B1	20020101	30	Apparatus and method for enhancing data transfer rates using transfer contr	710/5	710/18;		Ellis; Jackson